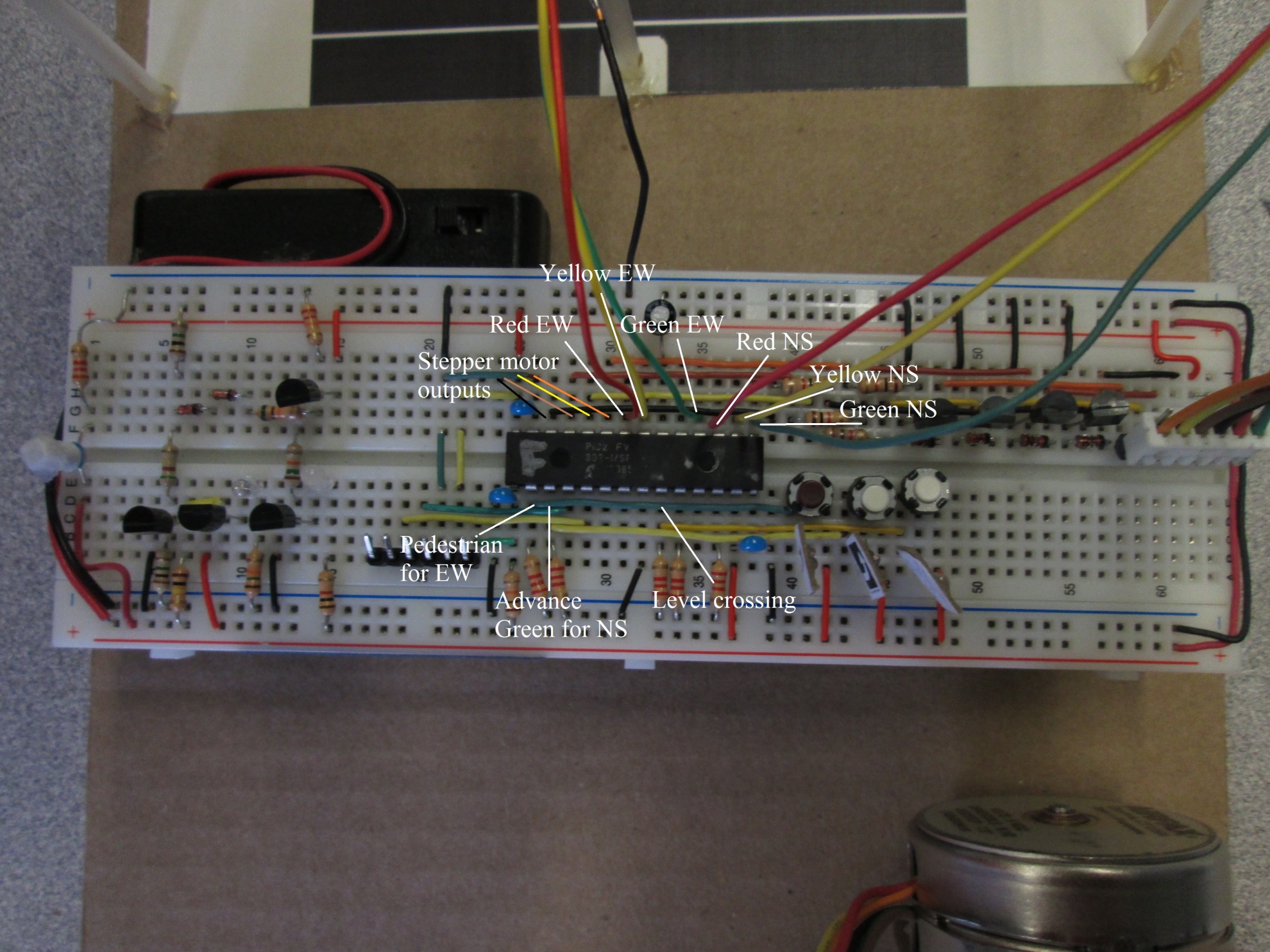
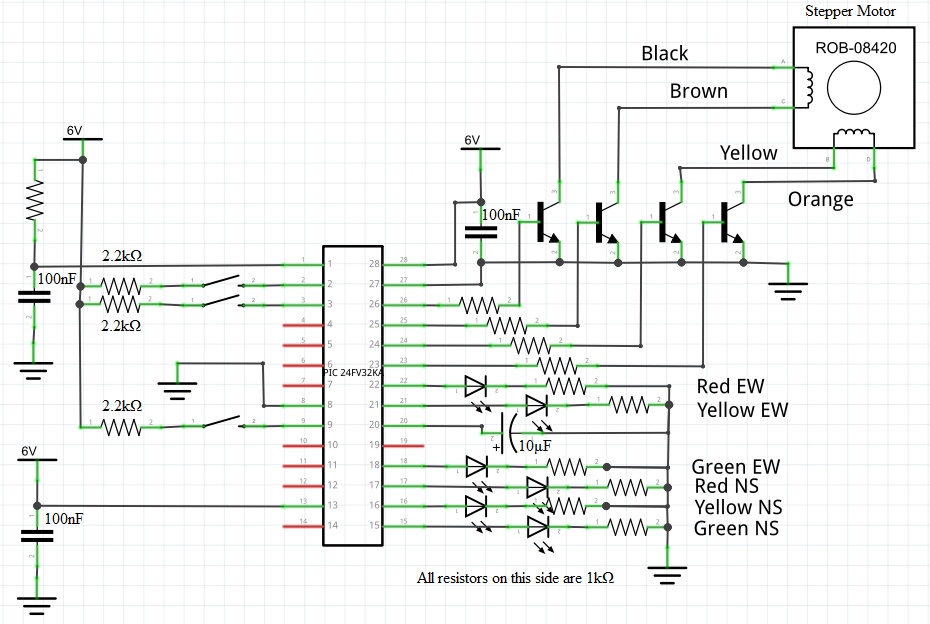
Circuit Diagram



Schematic